Millia Institute of Technology
Rambagh, Purnea

Affiliated to Bihar Engineering University, Patna

NAAC Accredited & ISO 9001:2015

SYLLABUS

Department of
Electronics & Communication Engineering

4th SEMESTER

8		Mathematics – III (Probability and Statistics)	3	1	0	4	4
9		Biology-I	2	1	0	3	3
10		Essence of Indian Knowledge Tradition/Indian Constitution				4	0
			TOTAL				23

104 – ELECTRONICS & COMMMUNICATION ENGINEERING

Semester IV [Second year] Branch/Course Electronics & Communication Engineering

sr. no.	CODE	Course Title	L	T	P	H	Credit
1		Digital Circuits	3	1	0	4	4
2		Digital Circuits Lab	0	0	2	2	1
3		Analog Circuits	3	0	0	3	3
4		Analog Circuits Lab	0	0	2	2	1
5		Semiconductor Physics and Devices	3	0	0	3	3
6		Semiconductor Physics and Devices Lab	0	0	2	2	1
7		Analog Communication	3	0	0	3	3
8		Analog Communication Lab	0	0	2	2	1
9		Electromagnetic Theory	3	1	0	4	4
10		OpenElective-1 / MOOC/SWAYAM Courses	3	0	0	3	2
11		Stress Management by Yoga (Non-Credit)	2	0	0	2	0
			TOTAL				23

105 – COMPUTER SCIENCE & ENGINEERING

Semester IV [Second year] Branch/Course: COMPUTER SCIENCE & ENGINEERING

sr. no.	CODE	Course Title	L	T	P	H	Credit
1		Discrete Mathematics	3	1	0		4
2		Computer Organization & Architecture	3	0	4		5
3		Operating Systems	3	0	4		5
4		Design & Analysis of Algorithms	3	0	4		5
5		Digital Electronics	3	0	4		5
6		Human Resource Development and Organizational Behavior	3	0	0		3
7		Environmental Science	3	0	0		0
			TOTAL				27

Electronics & Communication Engineering
IV Semester
Branch Code – 104

EC104	Digital Circuits	3L:1T:0P	3 Credits

Sl. No.	Contents	Contact Hours
1	Digital Principle : Analog vs Digital, Number system, Computer Codes, Digital Signals, Waveforms Positive and Negative logic, Logic Gate : basic, universal and others, Truth Table, Logic functions, IC Chips, Timing Diagram, Electrical analogy.	5
2	Boolean laws and theorems : Logic functions, Conversion of logic functions into truth table and vice versa. SOP and POS forms of representation, Canonical form, minterms and maxterms, Simplification of logic functions by theorems and Karnaugh's map, don't care conditions.	5
3	Analysis and synthesis of Combinational logic circuits: Comparators, Multiplexers, Encoder, Decoder, 7 Segment Display, Half Adder and Full Adder, Subtractors, Serial and Parallel Adders, BCD Adder	6
4	Sequential circuit blocks and latches : Flip-Flops-Race around condition, Master-Slave and Edge triggered SR, JK, D and T Flip Flop, Shift registers, Counters-Synchronous and Asynchronous: Design of ripple counter	10
5	Timing circuit : Multivibrators, Monostable and Astable timer: LM555	4
6	Integrated circuit logic families : RTL, DTL, TTL, CMOS, IIL/I2L (Integrated Injection logic and Emitter Coupled logic).	5
7	Use of building blocks : Designing larger systems such as Digital-to-Analog Converters (DAC) : Weighted resistors and R-2R, Analog-to-Digital(ADC)-converter, counter and succession.	5
	Total	40

Sl. No.	Name of Authors / Books /Publishers
1	"Digital Fundamentals", Floyd and Jain., Pearson
2	"Digital Logic and Computer Design", M.Morris Mano, Pearson
3	"Fundamentals of Digital Circuits", A.Anand Kumar, PHI
4	"Digital Systems", Ronald J.Tocci, Neal S.Widmer, Pearson

Digital Circuits and Systems Lab are according to the theory mentioned above.	**0L: 0T: 2P**	**1 Credit**

EC105	Analog Circuits	3L:0T:0P	3 Credits

Sl. No.	Contents	Contact Hours
1	Small signal amplifiers : CB, CE, CC configurations, hybrid model for transistor at low frequencies, RC coupled amplifiers, mid band model, gain and impedance, comparisons of different configurations, Emitter follower, Darlington pair (derive voltage gain, current gain, input and output impedance). Hybrid-model at high frequencies (pi - model).	7
2	Multistage Amplifiers : Cascade and Cascode amplifiers, Calculations of gain, impedance and bandwidth. Design of multistage amplifiers. Feedback Amplifiers: Feedback concept, Classification of Feedback amplifiers, Properties of negative Feedback amplifiers, Impedance considerations in different configurations. Analysis of feedback Amplifiers.	11
3	Field Effect Transistor : Introduction, Classification, FET characteristics, Operating point, Biasing, FET small signal Model, Enhancement and Depletion type MOSFETs, FET Amplifier configurations (CD,CG and CS).	7
4	Oscillators : Barkhausen criterion , Sinusoidal Oscillators, the RC phase-shift oscillator, resonant circuit Oscillators, a general form of oscillator circuit, the Wien -bridge oscillator, Crystal oscillators, Hartley, Colpitt's and Clapp's Oscillator.	8
5	Power Amplifiers : Power dissipations in transistors, Amplifiers Classification, (Class-A, Class-B, Class-C, Class-AB) Efficiency analysis, Push-Pull and Complementary Push-pull amplifiers, Cross over distortion and Harmonic distortion in Push-Pull amplifier. Tuned amplifiers (single, double and stagger tuned amplifier).	6
	Total	40

Sl. No.	Name of Authors / Books /Publishers
1	"Electronic Devices and Circuit Theory", Boylestad and Nashelsky, PEARSON PUBLICATION.
2	"Electronic devices and circuits", Salivahanan, Suresh Kumar, Vallavaraj, TMH, 1999
3	"Integrated Electronics, Analog and Digital Circuits and Systems", J. Millman and Halkias, TMH, 2000
4	"Micro Electronic Circuits", Sedra and Smith, Oxford University Press, 2000
5	"Electronic Devices and Circuits", David A Bell, Oxford University Press, 2000

Analog Circuits Lab are according to the theory mentioned above.	0L: 0T: 2P	1 Credit

EC106	Semiconductor Physics and Devices	3L:0T:0P	3 Credits

Sl. No.	Contents	Contact Hours
1	Basics of Semiconductor Physics : Semiconductor carrier modelling- Bonding model, Energy band model, Carriers, Band gap, Carrier properties (Effective mass, Intrinsic carrier concentration, Doping), Density of states, Fermi function, Equilibrium carrier concentration (formula for n and p and np product), Charge neutrality relationship, Determination of Fermi level, Carrier concentration, Temperature dependence. Carrier Action - Drift, Mobility, Drift Current, Resistivity, Diffusion Current, Total current, Relation between the diffusion constants and mobility (Einstein's relationship), Recombination-Generation (Band- to-Band, R-G Centres, Auger, Impact Ionization). Equation of state, Continuity equation, Minority Carrier Diffusion Equation.	10
2	PN Junction Diode : Step junction, Built-in potential, Depletion width, Depletion Approximation, Electrostatic relationship (Charge density, Depletion width, Potential, Electric field) for Va = 0, Va ¡ 0 and Va ¿ 0, Ideal Diode Equation (Qualitative and Quantitative derivation : Band Model, Assumptions, Approximation, Boundary condition), Deviation from Ideal (R-G Current, Series resistance, High Level Injection), Junction Breakdown (Avalanche and Zener), Reverse Bias Junction Capacitance, forward Bias Diffusion Capacitance, Qualitative understanding of Turn on and Turn-off transients. Zener Diode, Tunnel diode, Varactor diode, Schottky diode.	10
3	Physics and technologies of BJT : Operational considerations, Modes and Configurations, Performance Parameters (Emitter Efficiency, Base Transport Factor, Common Base Current Gain, Common Emitter Current Gain and their derivation for an ideal transistor, Deviation from ideal (Base Width Modulation Punch Through, Avalanche Breakdown, Geometrical effects, R-G current), Small signal modelling.	6
4	Physics and technologies of FET : JUNCTION FET (Theory of operation, I-V relationship), MOS CAPACITOR (Energy Band diagram, Gate-Voltage relationship, Capacitance-Voltage characteristics), MOSFET (Theory of operation, Threshold voltage, I-V characteristics), NON IDEAL MOS (M-S work function difference, oxide charges, threshold adjustment and considerations)	6
5	Introduction to UJT, SCR, Triac and Diac (Construction, Working, Characteristics and Application), UJT Relaxation oscillator. Optoelectronic Devices : Photo diodes (PIN and Avalanche), Solar cell, LED, Solid State LASER diodes.	8
	Total	40

Sl. No.	Name of Authors / Books /Publishers
1	"Semiconductor Device Fundamentals", by R. F. Pierret, Addison-Wesley publishing company, 1996
2	"Semiconductor Physics and Devices: Basic Principles", by Donald A. Neamen, 3rd Edition, 2003
3	"Physics of Semiconductor Devices" S. M. Sze, 2nd edition, 1981

Semiconductor Physics Lab are according to the theory mentioned above.	0L: 0T: 2P	1 Credit

EC107	Analog Communication	3L:0T:0P	3 Credits

Sl. No.	Contents	Contact Hours
1	Introduction to the communication system : Block diagram of communication system and comparative study of analog and digital communication.	3
2	Modulation(upward frequency translation) and demodulation (downward frequency translation) and the need for modulation: broad classification of modulation [linear (amplitude-AM) and exponential (frequency-FM and phase-PM)]	7
3	Generation of double side band (DSB) with carrier, double side band with suppressed carrier (DSB-SC) and single side band with suppressed carrier: Demodulation of double side band with carrier –incoherent detector or envelope detector, peak diode detector, coherent or synchronous detection of DSBSC and single side band with suppressed carrier.	8
4	Superhetrodyne Receivers : Characteristics , Intermediate Frequency and its advantages, image rejection of the Receiver.	5
5	Generation of FM signals(direct and indirect methods) and Demodulation.	5
6	Noise: Different types of Noise, SNR in AM, FM and PM System and use of emphasis Circuit in FM for SNR optimization.	4
7	Analog pulse modulation : PAM, PWM, PPM and demodulation; comparative study of various analog pulse modulation	8
	Total	40

Sl. No.	Name of Authors / Books /Publishers
1	“Electronic Communication system”, by Kennedy. TMH.
2	“Communication system”, by Haykin, Wiley
3	“Communication system”, by Bruce carison . TMH.
4	“Modern Digital And Analog Communication”, B.P.LATHI Oxford

Analog Communication Lab are according to the theory mentioned above.	**0L: 0T: 2P**	**1 Credit**

EC108	Electromagnetic Theory	3L:1T:0P	3 Credits

Sl. No.	Contents	Contact Hours
1	Introduction to Vector Algebra, Coordinate Systems and Transformation, Vector Calculus. Electrostatics : Coulomb’s law, Gauss’s law and its applications, the potential functions, Equipotential surface, Poisson’s and Laplace’s equation, Applications (solution for some simple cases), Capacitance, Electrostatic energy, Conductor properties and boundary conditions between dielectricS and dielectric-conductor interface, Uniqueness Theorem.	10
2	Magnetostatics : Biot-Savart law, Ampere’s circuital law, Curl, Stoke’s theorem, Magnetic flux and magnetic flux density, Energy stored in magnetic field, Ampere’s force law, Magnetic vector potential, Analogy between electric and magnetic field.	6

3	Maxwell's equations, Equation of Continuity for time varying field. Inconsistency of Ampere's circuital law, Maxwell's equations in differential and integral form. Electromagnetic waves : Solution of wave equation in free space, Uniform plane wave propagation, Uniform plane waves, the wave equation for conducting medium, Wave propagation in lossless medium and in conductive medium, Conductors and dielectrics, Polarization	10
4	Reflections and Refractions : Reflection by a perfect conductor with normal as well as oblique incidence. Reflection and refraction by perfect dielectrics with normal and oblique incidence. Surface impedance. Poynting vector : Poynting theorem, Instantaneous, Average and Complex Poynting vector, Power loss in a plane conductor.	8
5	Transmission Lines : Transmission line theory, low loss radio-frequency and UHF transmission line. UHF line as a transformer, voltage step up of the quarter wave transformer. Transmission line chart (Smith Chart).	8
	Total	40

Sl. No.	Name of Authors / Books /Publishers
1	"Time-harmonic Electromagnetic Fields", R. F. Harrington, Wiley-IEEE Press, 2001
2	"Fields and Waves in Communication Electronics", Ramo, S., Whinnery, J.R., and Van Duzer, T., 3rd Ed., John Wiley and Sons, 1994
3	"Advanced Engineering Electromagnetics", Balanis, C.E., Wiley India Pvt. Ltd., Reprint, 2008
4	"Microwave Engineering", Pozar, D.M., 3rd Ed., John Wiley and Sons, 2004

Computer Science Engineering
IV Semester
Branch Code - 105

PCC CS 401	Discrete Mathematics	3L:1T:0P	4 Credits

Objectives of the course

Throughout the course, students will be expected to demonstrate their understanding of Discrete Mathematics by being able to do each of the following:

1. Use mathematically correct terminology and notation.
2. Construct correct direct and indirect proofs.
3. Use division into cases in a proof.
4. Use counter examples.
5. Apply logical reasoning to solve a variety of problems.

Detailed contents: